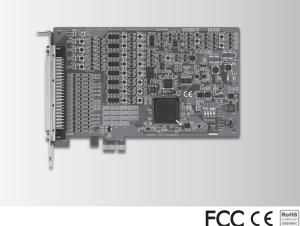
# **PCIE-1812**

## 250 kS/s, 16-bit, 8-ch, Simultaneous Sampling Multi-function PCI Express **DAQ Card**



## Features

- 8 differential simultaneous sampling analog inputs, up to 250 kS/s, 16-bit resolution
- 2 analog outputs, up to 3 MS/s, 16-bit resolution
- Full auto-calibration
- 2 analog triggers and 2 digital triggers for AI/O
- 32 programmable DI/Os with interrupt functions
- Four 32-bit programmable counters/ timers/ encoders
- Board ID switch
- Support for Microsoft Windows 10, 8 and 7

# Introduction

The PCIE-1812 is a simultaneous-sampling multifunction DAQ card to meet a wide range of application requirements. The device can simultaneously sample 8 AI channels with differential input configuration in order to achieve maximum noise elimination. It also provides 2-CH 16-bit analog outputs and with waveform generation capability, which can be performed together with analog input functions.

# **Specifications**

#### **Analog Input**

- Channels Differential Resolution 16 bits
- Sample Rate
- 250 kS/s max. Input Impedance
- 100GΩ/350pF Sampling Mode Software and external clock
- Innut Ranne

Gain	0.5	1	2	4	8
Bipolar	±10V	±5	±2.5	±1.25	±0.625
Unipolar	N/A	0~10	0~5	0~2.5	0~1.25
Absolute Accuracy (% of FSR)*	0.01	0.01	0.01	0.01	0.01

Software programmable

8-ch

#### **Analog Output**

- Channels .
- Resolution 16 bits
- **Output Rate** 3 M max. Autnut Rango Software programmable

2

5 mA

0.01%

16 bits

-10 V ~ +10 V

2

- Output Hunge	oonware p	rogrammabio
Internal Reference	Unipolar	0 ~ 5 V, 0 ~ 10 V
	Bipolar	-5 V ~ 5 V, -10 V ~ 10 V
External Reference		$0 \sim +x \lor @ -x \lor (-10 \le x \le 10)$
<ul> <li>Slew Rate</li> </ul>	20 V/µs	

Static update, Waveform Generation

Yes. Hysteresis range is configurable

Rising edge or falling edge, selected by software

Slew Rate
Dubulas Osashilita

- Driving Capability
- Operation Mode
- Accuracy

## **Analog Trigger**

- Channels
- Resolution
- Input Range Hysteresis
- Trigger Edge

## **Digital Trigger**

<ul> <li>Channels</li> </ul>	2
<ul> <li>Input Voltage</li> </ul>	Logic 0: 1.5 V max.
	Logic 1: 3.5 V min.
<ul> <li>Trigger Edge</li> </ul>	Rising edge or falling edge, selected by software

### **Digital I/O**

- Channels
- Input Voltage
- Output Voltage

#### **Counter/ Timer/ Encoder**

- Channels -
- Resolution
- Compatibility
- Max. Input Frequency
- **Counter/Timer Functions**
- Encoder Functions

### General

- Form factor
- **Operating Temperature**
- Storage Temperature
- Storage Humidity
- BoardID

# **Ordering Information**

PCIE-1812-AE

#### Accessories

- PCL-101100R-1E
- PCL-101100R-2E
- ADAM-39100-BE

32 (shared) Logic 0: 1.5 V max. Logic 1: 3.5 V min. Low 0.5 V max.@ +20 mA (sink) High 4.5 V min.@ -20 mA (source)

4 32 bits 5 V/TTL 10 MHz Frequency measurement, pulse width measurement, pulse output, PWM output Quadrature (X1, X2, X4), dual pulse (CW/CCW),

signed pulse (OUT/DIR)

PCI Express x 1 100-pin SCSI female connector 167 x 100 mm 0 ~ 60°C (32 ~ 140°F) (refer to IEC 68-2-1, 2) -40 ~ 70°C (-40 ~ 158°F) 5~95% RH non-condensing (refer to IEC 68-2-3) TM Switch

- 250 kS/s, 16-bit, 8-ch simultaneous sampling multifunction card
- 100-pin SCSI shielded cable, Female to Male, 1m 100-pin SCSI shielded cable, Female to Male,
- 2m 100-pin DIN-rail SCSI wiring board

AD\ANTECH **Data Acquisition Boards** 

All product specifications are subject to change without notice.

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I/O Connector

- Dimensions (L x W)